



#6 EOT/Elect
5/27/03
PATENT Moller
3430-0168P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Gee-Sung CHAE Conf.: 6251
Appl. No.: 09/972,963 ✓ Group: 2823
Filed: October 10, 2001 ✓ Examiner: BROOK KEBEDE
For: THIN FILM TRANSISTOR ARRAY SUBSTRATE
FOR LIQUID CRYSTAL DISPLAY DEVICE AND
METHOD OF MANUFACTURING THE SAME

REPLY TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 19, 2003

Sir:

In reply to the Restriction Requirement dated March 10, 2003, the following remarks are respectfully submitted in connection with the above-identified application.

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REMARKS

Claims 1-27 are pending in the present application.

The Examiner has required election in the present application between:

Group I, claims 1-7 and 17-22, drawn to a semiconductor device, classified in Class 257, subclass 59+; and

Group II, claims 8-16 and 23-27, drawn to a method of fabricating a semiconductor device, classified in Class 438, subclass 149.

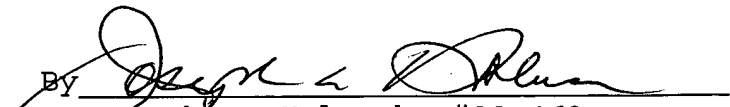
For the purpose of examination of the present application, Applicants elect, with traverse, Group I, claims 1-7 and 17-22.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Esther H. Chong (Reg. No. 40,953) at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Joseph A. Kolasch, #22,463

^{gm}
JAK:EHC/mks
3430-0168P

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000